

PATENT COOPERATION TREATY

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To: HAYASE, Kenichi HAYASE & CO. 13F, NISSAY SHIN-OSAKA Bldg., 3-4-30, Miyahara, Yodogawa-ku, Osaka-shi, Osaka 532-0003 JAPAN		PCT WRITTEN OPINION OF THE ISA (PCT Rule 43bis)	
		Date of Mailing January 11, 2005	

Applicant's or agent's file reference P33415-P0		See item 2 below for the subsequent procedure	
International application No. PCT/JP2004/014669	International filing date September 29, 2004	Priority date September 29, 2003	
International Patent Classification (IPC) or national classification and IPC Int. Cl ⁷ G11B20/14, G11B7/005			
Applicant Matsushita Electric Industrial Co., Ltd.			

1. This opinion contains indications relating to the following items:

- I ☒ Basis of the opinion
- II ☐ Priority
- III ☐ Non-establishment of report with regard to novelty, inventive step or industrial applicability
- IV ☐ Lack of unity of invention
- V ☒ Reasoned statement under Rule 43.2.1(a)(i) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement
- VI ☒ Certain documents cited
- VII ☐ Certain defects in the international application
- VIII ☐ Certain observations on the international application

OMISSION(2 and 3)

Date of completion of this opinion

December 17, 2004

Name and mailing address of the ISA/JP Japanese Patent Office	Authorized officer
	Telephone No.

I . Basis of the opinion

1. This opinion has been drawn on the basis of the language of international application, unless otherwise indicated below.

OMISSION(2, 3, and 4)

TRANSLATION of related part of Form PCT/ISA/237

WRITTEN OPINION OF THE ISA

International application No.
PCT/JP2004/014669

V Reasoned statement under Rule 43,2.1(a)(i) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement

1. STATEMENT

Novelty (N)	Claims 1-7	YES
	Claims NONE	NO
Inventive Step(IS)	Claims 1-7	YES
	Claims NONE	NO
Industrial Applicability (IA)	Claims 1-7	YES
	Claims NONE	NO

2. CITATIONS AND EXPLANATIONS

Claims 1-7

None of the literatures cited in the International Search Report describes a construction including a frequency conversion circuit for converting a first-cycle wobble binarized signal into a second cycle wobble binarized signal, a selector for selecting either the output of the frequency conversion circuit or the inputted second cycle wobble binarized signal, and a PLL circuit for generating a recording clock from the output of the selector, and this construction is not obvious to those in the art.

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International application No.
PCT/JP2004/014669

VI Certain documents cited

1. Certain published documents (Rule 43,2.1 and 70.10)

Application No. <u>Patent No.</u>	Publication date <u>(day/month/year)</u>	Filing date <u>(day/month/year)</u>	Priority date(valid claim) <u>(day/month/year)</u>
JP2004-246948 A [P, A]	2 September 2004 (02.09.2004)	12 February 2003 (12.02.2003)	

2. Non-written disclosures (Rule 43,2.1 and 70.9)

OMISSION

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